

LISTING OF THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Claim 1 (Canceled)

Claim 2 (Previously Presented)

A bandgap voltage reference circuit comprising:

a first bipolar transistor providing substantially a reference voltage V_{be} ;

a current mirror circuit comprising two bipolar transistors coupled in a current mirror arrangement for providing a voltage difference ΔV_{be} comprising substantially a difference signal between the respective V_{be} voltages of the two bipolar transistors; and

a comparator having respective inputs receiving voltages representative of V_{be} and ΔV_{be} and an output coupled to the base of the first bipolar transistor whereby a voltage representative of the sum of respective constants multiplying V_{be} and ΔV_{be} is provided at the output of the comparator.

Claim 3 (Currently Amended)

A bandgap voltage reference circuit comprising:

a first circuit providing a first voltage representative of V_{be} of a first bipolar transistor;

a second circuit current mirror circuit comprising two additional bipolar transistors coupled in a mirror arrangement for providing a second voltage ΔV_{be} representative of the difference of the two V_{be} voltages of the two additional bipolar transistors; and

a comparator having respective inputs receiving voltages representative of V_{be} and ΔV_{be} and an output coupled to the base of the first bipolar transistor whereby a substantially temperature independent voltage reference is provided at the output of the comparator.